STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: 40296-0032

Applicant:

Ji Eun JANG

Confirmation No.: 6545

Appl. No.:

10/629,672

Examiner: Unassigned

Filing Date: July 30, 2003

Art Unit: 2818

Title:

TEST MODE CIRCUIT OF SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56 and 37 CFR §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08A is a listing of documents known to applicant in order to comply with applicant's duty of disclosure pursuant to 37 C.F.R. §1.56 and §1.97. A copy of each of the listed documents are being submitted to comply with the provisions of 37 C.F.R. §1.97-1.99.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or is considered to be material to patentability as defined in 37 C.F.R. §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a prima facie prior art reference against the claims of the present application.

RELEVANCE STATEMENT

The foregoing document came to the Applicant's attention during a search of the corresponding Korean patent application. A copy of the translated Notice of Rejection from the Korean Patent Office is attached.

An English language translation of these documents are not readily available; however, the absence of such translations do not relieve the PTO from its duty to consider the submitted documents (37 CFR §1.98 and MPEP §609). A copy of the English abstract for each reference is attached.

TIMING/FEE

The instant Information Disclosure Statement is being filed before the mailing of a first Office Action on the merits under 37 CFR §1.97(b)(3), therefore, no fee is required in connection with its filing. However, the Commissioner is hereby authorized to charge any deficiency or to credit any overpayment to Deposit Account No. 08-1641.

Applicant respectfully requests that the listed documents be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08A be returned in accordance with M.P.E.P. §609.

Respectfully submitted,

Date: 3/8/05

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Approved for use through 10/31/2002. OMB 0651-0031

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Substitut	e for form 1449A/PT(Complete if Known			
				Application Number	10/629,672		
INFO	RMATION	DIS	CLOSURE	Filing Date	July 30, 2003		
STATEMENT BY APPLICANT				First Named Inventor	Ji Eun JANG		
				Group Art Unit	2818		
	(use as many she	eets as	necessary)	Examiner Name	Unassigned		
Sheet	1	of	1	Attorney Docket Number	40296-0032		

	U.S. PATENT DOCUMENTS							
Examiner Initials *	Cite No.1	U.S. P	atent Document	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevan		
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	FOREIGN PATENT DOCUMENTS							
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		Office ³	Number ⁴	Kind Code ⁵ (if known)			Relevant Passages or Relevant Figures Appear	Т ₆
	B01	JP	09-298221		YAMAHA CORP.	11/18/1997		
	B02	JP	01-095529		MITSUBISHI ELECTRIC CORP.	4/13/1989		
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Examiner Signature	Date Considered	

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 6 Applicant is to place a check mark hee if English language Translation is attached.